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DOCKET NUMBER: AUS920020592US1 DATE: SUNT DE 2003

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For: Apparatus and Method of Worldling/Bitline Redundancy Control Using Shift Registers in an SPRAM Inventors: Toru Asano, Sang Hoo Dhong, Takaaki Nakazato, and Osamu Takahashi Transmitted herewith for filing is the Patent Application of:

Assignee Name: International Business Machines Corporation

Assignee Residence: Armonk, New York Assignee Name: Toshiba America Electronic Components, Inc

Assignee Residence: Irvine, California

Assignee Name: Kabushiki Kaisha Toshiba

Assignee Residence: Tokyo, Japan

Patent Specification including an Abstract Enclosed are:

Sheet(s) of drawings. __Informal _X_Formal X 4 Pages of Claims

Form PTO 1595 and Assignment of the Invention to IBM Corporation Form PTO 1595 and Assignment of the Invention to Toshiba America Electronic Components Declaration(s) and Power of Attorney

× 2 2 × × × × – Form PTO 1595 and Assignment of the Invention to Kabushiki Kaisha Toshiba

Information Disclosure Statement, PTO 1449 and copies of ______references. A certified copy of a ____ application.

| X_ | A certified copy of a | Statement, PTO 1449 | | Basic Fee | |
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| 100 | sims | 3 | | l Filing Fee = \$ 750 | |
| Total Cla | · ·· Claims | 00 | Tota | 11 | |
| Indepen | dent Claims | | | | |
| Multipl | le Dependent Claims | | | | |



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Respectfully submitted,

Robert M Carwell

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